

**DECLARATION FOR NON-PROVISIONAL PATENT APPLICATION***

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below at 201 et seq. beneath my name.

I believe I am the original, first and sole inventor if only one name is listed at 201 below, or an original, first and joint inventor if plural names are listed at 201 et seq. below, of the subject matter which is claimed and for which a patent is sought on the invention entitled:

INTEGRATED CIRCUIT AND RELATED IMPROVEMENTS

and for which a patent application and for which a patent application

- is attached hereto.
- which is identified by Docket No. 85170-4800
- which was filed on October 6, 2003 as Application No. _____

I hereby state that I have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

EARLIEST FOREIGN APPLICATION(S), IF ANY, FILED PRIOR TO THE FILING DATE OF THE APPLICATION				
APPLICATION NUMBER	COUNTRY	DATE OF FILING (day, month, year)	PRIORITY CLAIMED	
0108807.9	Great Britain	April 7, 2001	YES <input checked="" type="checkbox"/>	NO <input type="checkbox"/>

I hereby claim the benefit under Title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below.

PROVISIONAL APPLICATION NUMBER	FILING DATE

* for use only when the application is assigned to a company, partnership or other organization.

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code Section 112, I acknowledge the duty to disclose information known to me which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

NON-PROVISIONAL APPLICATION SERIAL NO.	FILING DATE	STATUS		
		PATENTED	PENDING	ABANDONED
PCT/GB02/01407	March 27, 2002		X	

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

2 0 1	FULL NAME OF INVENTOR	LAST NAME PARKES	FIRST NAME Stephen	MIDDLE NAME Maxwell	
	RESIDENCE & CITIZENSHIP	CITY Dundee	STATE OR FOREIGN COUNTRY Great Britain	COUNTRY OF CITIZENSHIP Great Britain <i>AM</i> Germany <i>7/27/04</i>	
	POST OFFICE ADDRESS	STREET University of Dundee Nethergate, Perth Road	CITY Dundee	STATE OR COUNTRY Great Britain	ZIP CODE DD1 4HN
		SIGNATURE OF INVENTOR 201 <i>X S. M. Parkes</i>		DATE <i>X</i>	20/11/2003
2 0 2	FULL NAME OF INVENTOR	LAST NAME	FIRST NAME	MIDDLE NAME	
	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP	
	POST OFFICE ADDRESS	STREET	CITY	STATE OR COUNTRY	ZIP CODE
		SIGNATURE OF INVENTOR 202		DATE	

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
17 October 2002 (17.10.2002)

PCT

(10) International Publication Number
WO 02/082653 A2

(51) International Patent Classification⁷: **H03K 19/177**

(21) International Application Number: **PCT/GB02/01407**

(22) International Filing Date: 27 March 2002 (27.03.2002)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
01 08 807.9 7 April 2001 (07.04.2001) GB

(71) Applicant (for all designated States except US): **THE UNIVERSITY COURT OF THE UNIVERSITY OF DUNDEE [GB/GB]**; Nethergate, Perth Road, Dundee DD1 4HN (GB).

(72) Inventor; and

(75) Inventor/Applicant (for US only): **PARKES, Stephen, Maxwell [GB/GB]**; c/o The University of Dundee, Nethergate, Perth Road, Dundee, DD1 4HN (GB).

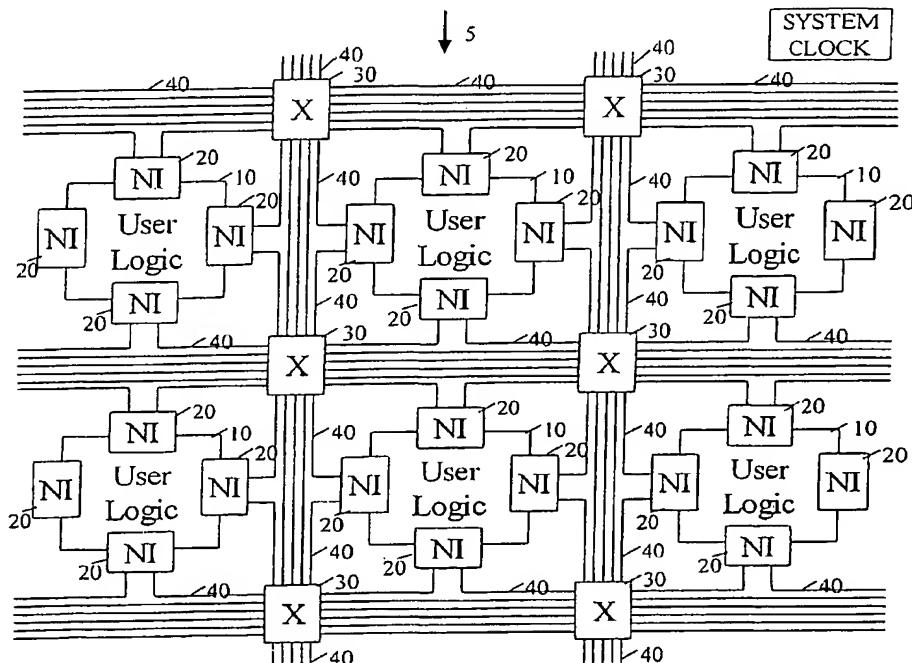
(74) Agents: **MCCALLUM, William, Potter et al.**; Cruikshank & Fairweather, 19 Royal Exchange Square, Glasgow G1 3AE (GB).

(81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZM, ZW.

(84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

[Continued on next page]

(54) Title: INTEGRATED CIRCUIT AND RELATED IMPROVEMENTS



WO 02/082653 A2

(57) Abstract: There is disclosed an improved integrated circuit (5) and a related system apparatus and method. The integrated circuit (5) comprises: a plurality of logic area of user logic areas (10); and an actively switchable network capable of selectively connecting at least one logic area (10) with another logic area (10).